

AA8-9 SOJ22GL

8 Elements Avalanche Photodiode Array

Special characteristics:

- quantum efficiency >80% at λ 760-910 nm
- high speed, low noise
- good uniformity between elements
- low cross talk
- with NTC as temperature control device



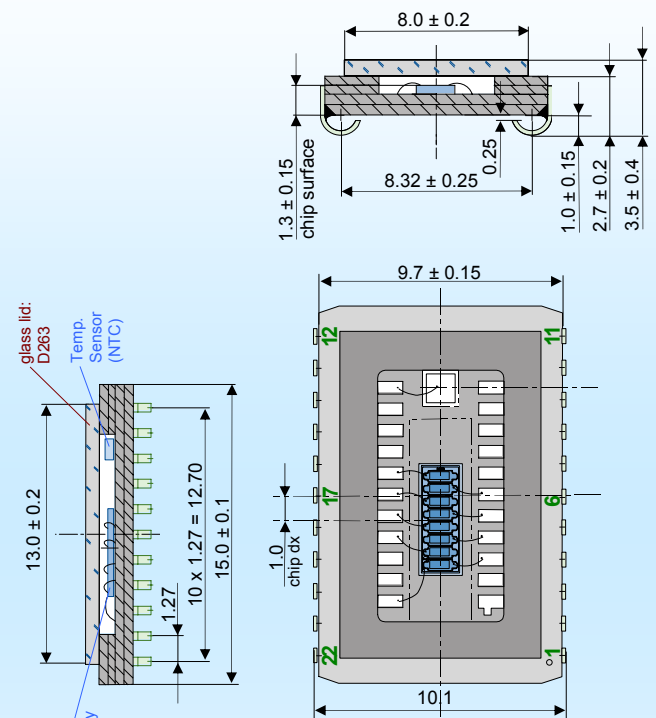
Parameters:	AA8-9 SOJ22GL
no. of Elements	8
Active Area / Element [μm]	1000 x 405
Gap / Separation [μm]	95
Pitch [μm]	500
Spectral Range	450 ... 1050
Spectral Responsivity ¹⁾ (at 905 nm, M = 100)	min. 55 A/W typ. 60 A/W
Max. Gain (I _{po} = 1nA)	typ. 100
Dark Current ¹⁾ (M = 100)	typ. 5 nA
Capacitance ¹⁾ / Element (M=100)	typ. 2 pF
Breakdown Voltage U _{BR} (at I _D = 2 μA)	100 ... 300 V
Rise Time at 905 nm, 50 Ω	typ. 2 ns
Cross-talk (at 905 nm)	typ. 50 dB
Photo Current Uniformity (at M = 50)	$\pm 20\%$ typ. $\pm 5\%$
Dark Current Uniformity (at M = 50)	$\pm 20\%$ typ. $\pm 5\%$
Resistance at +25 °C ²⁾	10 k Ω
Alpha Value at 25 °C ²⁾	-4.39 %/°C
Beta Value 25/85 ²⁾	3976 K
Operating Temperature	-20 ... +70 °C
Storage Temperature	-40 ... +100 °C

1) Measurement conditions:
Setup of photo current 2.0 nA at M = 1 and irradiation by a IRED (880 nm, 80 nm bandwidth).
Increase the photo current up to 100 nA, (M = 50) by internal multiplication due to an increasing bias voltage.

2) Technical parameter of the temperature control device (NTC).

RoHS compliant

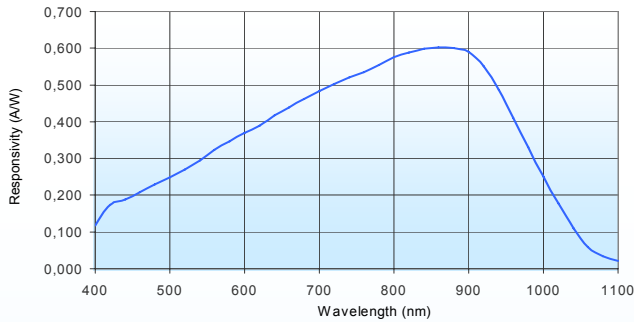
Package SOJ22GL (AA8-9):



Pin no.	Function	Pin no.	Function
1	Common Anode	12	Temp.-Sensor
2	n.c.	13	n.c.
3	Cathode 1	14	n.c.
4	Cathode 3	15	n.c.
5	Cathode 5	16	Cathode 8
6	Cathode 7	17	Cathode 6
7	n.c.	18	Cathode 4
8	n.c.	19	Cathode 2
9	n.c.	20	n.c.
10	n.c.	21	n.c.
11	Temp.-Sensor	22	guard ring

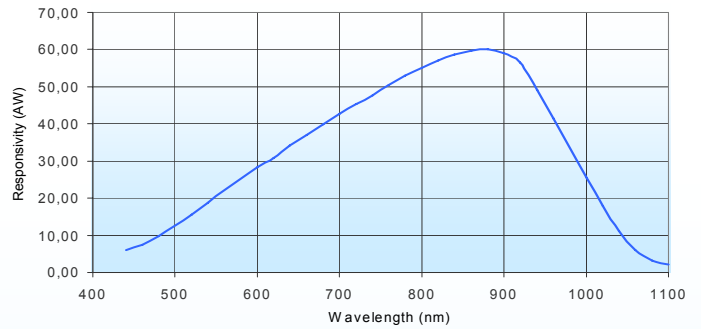
Spectral Responsivity at M = 1

Series - 9



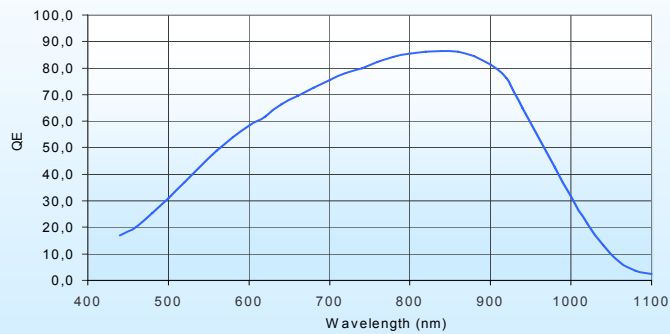
Spectral Responsivity at M = 100

Series - 9



Quantum Efficiency for M = 100

Series - 9



Maximum Ratings:

- max. electrical power dissipation 800 mW at 22°C
- max. optical peak value, once 400 mW for 1 s
- max. continuous optical operation $I_{Ph} (DC) \leq 250 \mu A$
 $\leq 1 \text{ mA}$ for signal 50 μs "on" / 1 ms "off"
- $(P_{electr.} = P_{opt.} * S_{abs} * M * U_R)$

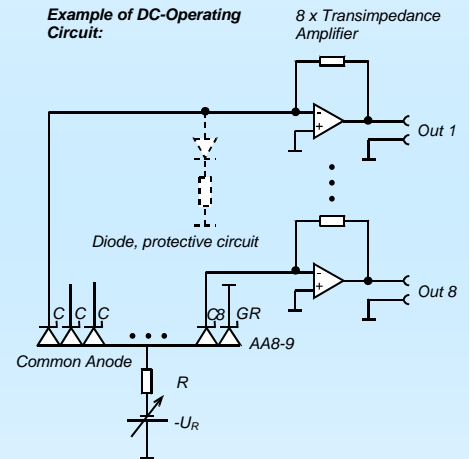
Application Hints:

- Current should be limited by a protecting resistor or current limiting - IC inside the power supply.
- Use of low noise read-out - IC.
- Use temperature sensor for temperature compensation of the bias voltage.

Handling Precautions:

- SMD mounting: Lead free soldering possible.
- Soldering temperature: The maximum temperature at the component must not exceed 250 °C. The range over 220 °C should not take more than 1 min. The device must be protected against solder flux vapour!
- ESD – protection: Standard precautionary measures are sufficient.
- Storage: Devices stored in Trays or (for bigger volumes) Embossed Carrier Tape
Maximum storage time 12 months.
- Avoid skin contact with window!
- Clean window with Ethyl alcohol if necessary.
- Do not scratch or abrade window.

Example of DC-Operating Circuit:



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